

ABSTRACT OF THE DISCLOSURE

A voltage generating circuit for generating an output voltage according to an input voltage is arranged to be able to generate the output voltage with desired
5 rise characteristics. The voltage generating circuit includes a resistor circuit that is serially implemented with respect to the input voltage, a condenser unit that cooperates with the resistor circuit to generate the output voltage, a digital delay circuit that delays at
10 least one of a rise and a fall of the input voltage and generates a delay output based thereon, and a bypass circuit that controls bypassing of a predetermined resistor included in the resistor circuit according to the delay output from the digital delay circuit.

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